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## Lecture 8

17 FEB 2014

We finished off the universality proof,  
 $n$ -qubit  
 that any unitary  $U$  can be  
 simulated w/ gates from a  
 discrete gate set, using for example  
 $\{CNOT, H, T\}$ , up to accuracy  
 $\epsilon$  w/ no more than

$$O(n^2 t^n \log^c \left( \frac{n^2 t^n}{\epsilon} \right)) \text{ gates.}$$

(This is b/c the algorithm to  
 decompose an arbitrary  $n$ -qubit  
 unitary into CNOTs + single qubit  
 unitaries gives a circuit w/  $n^2 t^n$  gates  
 - overhead from exponential size of  
 matrix  $U$  & Gray code construction  
 - combined w/ overhead from  
 Solovay-Kitaev & the need for  
 each gate to have  $\frac{\epsilon}{L}$   
 accuracy where  $L$  is the # of gates)

(2)

We would like to argue that there are unitaries acting on  $n$  qubits that cannot be approximated w/ polynomial-size quantum circuits.

- We will do so by arguing that

there are states that cannot be realized by polynomial size q- circuits.

First, let's figure out how many states we can reach w/ a circuit w/  $m$  gates

(Suppose we have  $g$  different kinds of gates & each one acts on no more than  $f$  qubits,

E.g.,  $\{CNOT, H, T\}$  would have  $g = 3, f = 2$ .)

For the first gate, there are no more than

$$\binom{n}{f}^g = O(n^f)^g = O(n^{fg})$$

possible choices.

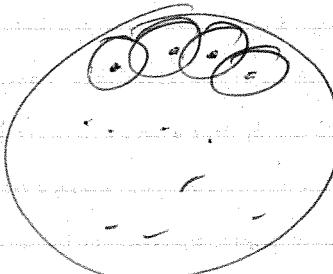
(3)

Then no more than  
 $O(n \lg m)$  possible states  
can be computed w/  $m$  gates.

Now suppose that we wish to  
approximate a state  $| \psi \rangle$  on  
 $n$  qubits up to  $\epsilon_{\text{go}}$ .

We can cover the space of all  
n-qubit pure states w/ an  $\epsilon$ -net  
(discretization of Hilbert space).

~~Diagram~~



Important question: How many states  
do we require to have an  $\epsilon$ -net?  
In dimensional

(4)

for dimension  $d$

An  $\epsilon$ -net<sup>13</sup> is defined to be a set of  $M$  pure states  $\{|\psi_i\rangle\}_{i=1}^M$  such that for all  $|\psi\rangle \in \mathbb{C}^d$ ,  
pure states

$\exists i \in \{1, \dots, M\}$  such that

$$\||\psi\rangle - |\psi_i\rangle\|_2 \leq \epsilon$$

~~Bound on Size?~~

~~For~~ every state in  $d^{2^n}$  dimensions  
has  $2^n$  amplitudes being  $a_j$

$$\sum_{j=1}^{2^n} |a_j|^2 = 1$$

$$= \sum_{j=1}^{2^n} (x_j^R)^2 + (d_j^I)^2 = 1$$

↓ equation for  
a sphere in

~~The~~ surface area of ~~is~~ a sphere  $2 \cdot 2^n$  dimensions  
of radius  $r$  in  $d$  dimensions is

$$S_d(r) = 2\pi^{d/2} r^{d-1} / \Gamma(d/2)$$

can approximate surface area of radius  $\epsilon$   
sphere by volume of radius  $\epsilon$  in one less dimension

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$$V_d(r) = \frac{2\pi^{d/2} r^d}{d \Gamma(d/2)}$$

so we need a number of patches  
covering the space to go like

$$\frac{S_{2^{n+1}}(1)}{V_{2^{n+1}}(\varepsilon)} = \frac{\sqrt{\pi} \Gamma(2^{n+1}/2)}{\Gamma(2^n)} \frac{[2^{n+1}-1]}{\varepsilon^{2^{n+1}-1}}$$

there is the inequality  $\Gamma(2^{n+1}/2) \geq \Gamma(2^n) \cdot 2^n$

$$\Rightarrow \text{ratio} \geq N \left( \frac{1}{\varepsilon^{2^{n+1}-1}} \right)$$

i.e., # of patches need to cover  
space grows doubly exponentially  
fast w/ n

(i.e., # of states is growing this  
way too)

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In order to reach all the states in the  $\epsilon$ -net of a circuit of  $m$  gates, we would require

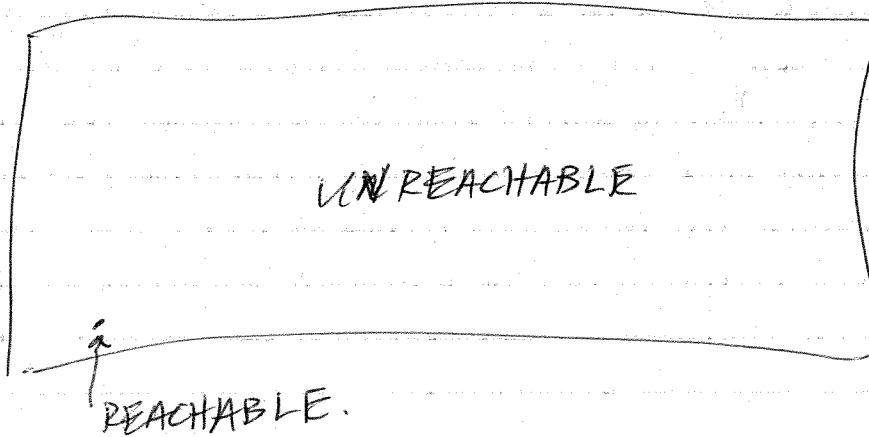
$$O(n^{\text{fpm}}) \geq N\left(\left(\frac{1}{\epsilon}\right)^{2^{n+1}-1}\right)$$

(i.e., # of states we can reach should be larger than the size of the  $\epsilon$ -net.)

$$\Rightarrow m = \underline{N\left(2^n \log\left(\frac{1}{\epsilon}\right)\right)} / \log(n)$$

we would need an exponential number of gates.

So these states are difficult to reach & of course there are many of them. Suggests the following picture of Hilbert space



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We can now formally define

BQP as

Let  $A = \text{Ayes} \cup \text{Ans} \subseteq \{0, 1\}^*$

$A \in \text{BQP}$  if  $\forall x \in A \exists$

deterministic

a polynomial-time Turing machine

that generates a description

of a quantum circuit  $Q_x$  acting  
on  $p(n)$  qubits such that

(circuit  
elements  
should  
be from  
some  
universal  
family.)

(Completeness) If  $x \in \text{Ayes}$ , then

$$\Pr\{Q \text{ accepts } x\} =$$

$$\text{Tr}\left\{\left|1\rangle\langle 1\right| \otimes I^{\otimes(p(n)-1)} |U_x|_0\rangle\langle 0|^{(p(n))} |U_x|\right\}$$

$$\geq 2/3$$

(Soundness) If  $x \in \text{Ans}$ , then

$$\Pr\{Q \text{ rejects } x\} =$$

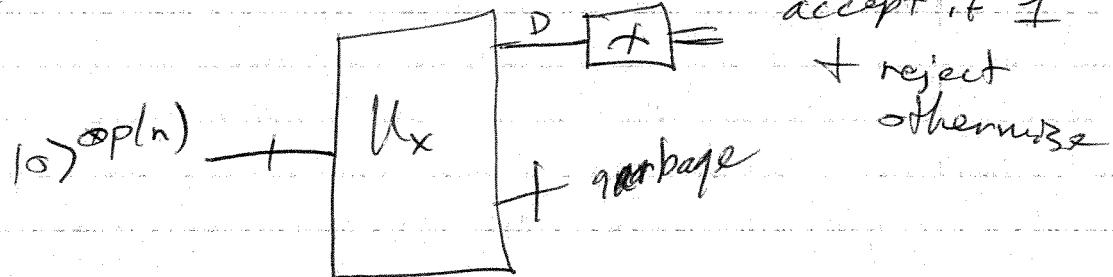
$$\text{Tr}\left\{\left|0\rangle\langle 0\right| \otimes I^{\otimes(p(n)-1)} |U_x|_0\rangle\langle 0|^{(p(n))} |U_x|\right\}$$

$$\geq 2/3$$

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Intuitive picture:  $x \rightarrow M(x) \rightarrow \text{output } Q_x$

Then



As in the case of BPP,  
we can amplify probabilities to  
be exponentially close to  
their extremes if

$$1 \downarrow x \in \text{Ayes} \Leftrightarrow \Pr[\text{accept}] \geq a(1/x)$$

$$a \downarrow x \in \text{Ans} \Leftrightarrow \Pr[\text{reject}] \leq b(1/x)$$

$$b \downarrow + a(n) - b(n) \geq \frac{1}{\text{poly}(n)}$$

class is robust under a wide variety  
of error parameters

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## BQP Subroutine Theorem:

What if we want to ~~not~~ use a BQP algorithm as a subroutine for some other algorithm?

This is commonly done in computer science, and we should understand how to do this properly w/ q. computers.

For example, we have circuit  $Q_x$ , for input  $x$ ,  
+ circuit  $Q_{x_2}$  for input  $x_2$

we can then devise a circuit  $U$   
such that

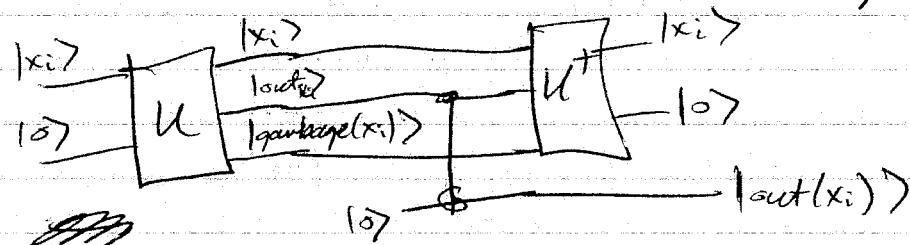
$$U|x_i\rangle|0\rangle \rightarrow |x_i\rangle|\text{out}(x_i)\rangle|\text{garbage}(x_i)\rangle$$

~~garbage~~

q

These are undesirable  
when we query in

To eliminate this problem, we "uncompute" e.g.



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w/ BQP defined, we can now prove a simple, yet important theorem

$$BQP \subseteq PSPACE$$

(better than obvious  
 $BQP \subseteq EXP$ )

Need to show the existence of a PSPACE algorithm for simulating any promise problem in BQP

Consider that any BQP circuit consists of  $U_1, \dots, U_r$  & acceptance probability is given by

$$\langle 0 |^{op(n)} U_1^\dagger \cdots U_L^\dagger | 1 \rangle \langle 1 |^{op(n-1)} U_L \cdots U_1 | 0 \rangle^{op(n)}$$

↑                   ↑  
 Insert identity matrices

$$\Rightarrow \langle 1 |^{op(n)} (U_L \cdots U_1) | 0 \rangle^{op(n)} = \langle 1 |^{op(n)} U_L I U_{L-1} \cdots U_2 I U_1 | 0 \rangle^{op(n)}$$

(12)

So

$$= \cancel{\sum_{Y_L+1}^L U_1^+ | Y_{2L} \rangle \langle Y_{2L} | U_2 \sum_{Y_{L-1}} Y_{L-1}^+ | Y_{2L-1} \rangle \langle Y_{2L-1} | U_3 \sum_{Y_1} Y_1^+ | Y_{2L-1} \rangle \langle Y_{2L-1} | U_4 | 10 \rangle}$$

$$= \sum_{Y_1, Y_2, \dots, Y_{2L}} (U_1^+ | Y_{2L} \rangle \langle Y_{2L} | U_2^+ | Y_{2L-1} \rangle \langle Y_{2L-1} | \dots$$

$$U_3^+ | Y_{2L-1} \rangle \langle Y_{2L-1} | U_4^+ | Y_1 \rangle \langle Y_1 | U_4 | 10 \rangle$$

$$\dots (Y_2 | U_2 | Y_1) (Y_1 | U_4 | 10)$$

each  $U_i$  acts on just one or two qubits, so from the description

of the circuit, it is easy to compute entries like

$$(Y_j | U_i | Y_{j-i})$$
 in polynomial time

& for each path we can store the result in polynomial space.

Since we can erase calculated terms after adding them to the running total, we can use only poly-space & have enough precision to decide whether to accept or

reject

as

Feynman

path

integral